

FOR IMMEDIATE RELEASE

SiliconPipe Adds Dr. Skip Fehr to its Advisor Team

Dr. Skip Fehr to lend his expertise to SiliconPipe

San Jose, California—April 24, 2006— SiliconPipe, an Open Source Intellectual Property Company, today announced that Dr. Skip Fehr, IC packaging industry veteran, has joined its advisory team, effective immediately. He has agreed to serve as advisor to the leading-edge interconnection technology development company.

Dr. Fehr was a co-founder and CTO of Integrated Packaging Assembly Corp., (IPAC and now OSE-USA), located in San Jose, CA. Prior to that, Dr. Fehr was director of IC package development and assembly at chipmaker LSI Logic in Milpitas, CA. He was among the first employees (number 32) at Intel before joining LSI (where he was among the first 10 employees). He has also held technical posts at several other leading semiconductor companies including Burroughs, Fairchild Semiconductor Inc., and Texas Instruments. Dr. Fehr received his Ph.D. in material science and engineering from Iowa State University.

“We are delighted to have Skip join us as an advisor” commented SiliconPipe’s co-founder, Joseph Fjelstad. “With his four decades of experience in the electronics industry, Skip’s knowledge of the IC packaging realm is unrivaled and we look forward to having his guidance as SiliconPipe moves to roll out its, low-cost, high-speed packaging concepts.”

Dr. Fehr’s role will include providing technological assessment and guidance on identifying appropriate development partners and markets for SiliconPipe’s novel high-speed packaging concepts.

About SiliconPipe

SiliconPipe, founded in 2002 in San Jose, California, is an Open Source Intellectual Property Company that designs and integrates complete high-performance system-interconnect solutions. SiliconPipe’s suite of novel patent-pending high speed copper based technologies includes Off the Top (OTT™) and Stair-Step (SSP) IC packaging solutions; low-skew parallel line interconnects for memories, AirCore™ cable and high-performance copper backplanes. These technologies are significant improvements over traditional interconnect concepts and provide cost-effective solutions that meet the performance requirements of next-generation high-speed products. SiliconPipe has over 120 patent submittals. For additional information about SiliconPipe, visit their website at www.siliconpipe.com.

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